



Date Sheet July 2010\19T

GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY

Kashmere Gate, Delhi-110403

EXAMINATION DIVISION

Conduct Branch-I

THEORY DATE SHEET FOR END TERM EXAMINATIONS (JULY, 2010)

M. Tech. (CSE, IT & VLSI Design)

Only for C-DAC Students

Old: 2006, 2007 & 2008 Batch

New: 2009 Batch

Timing: 10:00 AM to 1:00 PM

| Date/Day | Course Code | Course | Subject |
|-------------------------|--------------------|---------------|--------------------------------------------|
| 21.07.2010 Wednesday | VLSI Design | ITR-610 | Digital Signal Processing (Old/New) |

Examination Center: University School of Architecture & Planning, Guru Gobind Singh Indraprastha University
Campus, Kashmere Gate, Delhi-110403.

NOTE: BOOKS, ANY OTHER PRINTED / HANDWRITTEN / COURSE MATERIAL ETC., BAGS, MOBILE PHONES, PROGRAMMABLE CALCULATORS & OTHER ELECTRONIC GADGETS ARE NOT PERMITTED INSIDE THE PREMISES OF THE EXAMINATION CENTRE, IF SUCH MATERIAL(S) MAY BE BOOKED UNDER UNFAIR MEANS CASE.

For any clarification and query please contact:

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|-------------------------------------------------|---------------------|
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(A.D. Lamba)
Officer Incharge (conduct)

(Prof. Yogesh Singh)
Controller of Examinations