



Date Sheet/ Oct 2011

GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY

Sector 16-C, Dwarka, New Delhi - 110075

EXAMINATION DIVISION

Conduct Branch-I

PROPOSED PRACTICAL DATE SHEET FOR END TERM EXAMINATIONS (NOV-DEC 2011)

Programme: M. Tech. (CSE, IT and VLSI Design)

Only for C-DAC Students

Date/Day	Course/ Semester	Course Code	Time: 9:30 AM onwards	Course/ Semester	Course Code	Time: 2:00 PM onwards
28.11.2011 Monday	CSE/ 3rd Sem.	ITR-751	Multimedia Lab	VLSI/ 3rd Sem.	ITV-751	Lab based on Algorithm for VLSI Design automation
	IT/ 3rd Sem.	ITR-751	Lab based on Elective –I			
29.11.2011 Tuesday	CSE/ 3rd Sem.	ITR-753	NMS Lab	VLSI/ 3rd Sem.	ITV-753	Second Elective
	IT/ 3rd Sem.	ITR-753	Lab based on Elective –II			
30.11.2011 Wednesday	CSE/ 3rd Sem.	ITR-755	Lab based on Elective(s)	VLSI/ 3rd Sem.	ITV-755	Minor Project
	IT/ 3rd Sem.	ITR-755	Lab based on Elective – III			
01.12.2011 Thursday	CSE/ 3rd Sem.	ITR-757	Minor Project Work			
	IT/ 3rd Sem.	ITR-757	Minor Project			
02.12.2011 Friday	CSE/ 1st Sem	ITR-651	Algorithm Analysis Lab	VLSI/ 1st Sem.	ITV-651	DSD with Verilog Lab
	IT/ 1st Sem.	ITR-651	Algorithm Analysis Lab			
05.12.2011 Monday	CSE/ 1st Sem	ITR-653	Software Engineering Lab	VLSI/ 1st Sem.	ITV-653	Digital IC design Lab
	IT/ 1st Sem.	ITR-653	Software Engineering Lab			
06.12.2011 Tuesday	CSE/ 1st Sem	ITR-655	Advanced Computer Network Lab	VLSI/ 1st Sem.	ITV-655	AAD Lab
	IT/ 1st Sem.	ITR-655	Advanced Computer Network Lab			
07.12.2011 Wednesday	CSE/ 1st Sem	ITR-657	Data Base Management systems Lab			
	IT/ 1st Sem.	ITR-657	Data Base Management systems Lab			

Note: Any representation regarding the proposed dates/ paper etc. may be forwarded to the Controller of Examination / Officer Incharge (Conduct) through the Coordinator-cum-Convener of M. Tech. (CSE, IT & VLSI Design) program latest by 8th November, 2011.

For any clarification and query please contact:

Conduct Branch – I	011-25302263 011-25302264
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(A.D. Lamba)
Officer Incharge (Conduct)
Date: 31.10.2011

(Prof. Pravin Chandra)
Controller of Examinations